

VLSI Design Lab

Block No.4, Room No. 422, ECE Dept., MAIT



The VLSI Laboratory is well equipped with industry standard VLSI EDA front end and back end tools along with the hardware resources by Xilinx and Mentor Graphics. Licensed version of Vivado and Tanner EDA are available in this laboratory. The Laboratory provides hands on experience to the students in the field of analog and digital circuit design. Students can explore and practice design exercises by simulating and verifying the results using HEP-1 Tanner back-end tool, starting with schematic, spice circuit simulation and physical layout simulation of the design. The lab also provides Vivado software a front-end tool to simulate, synthesize and implement a digital Circuit. Also for the purpose of implementation of digital circuits PYNQ-Z2 FPGA boards are available in the lab.

Courses to cater:

- ETEC-354 VLSI Design lab

Facilities:

- Mentor Graphics HEP1 tools
- Vivado Software by Xilinx
- PYNQ-Z2 FPGA board