

Project Lab

Block No.4, Room No. 401(A), ECE Dept., MAIT



The objective of Project Lab is to motivate the students of UG program in ECE to make innovative projects keeping in mind the technological trends, Industry and society requirements. The experienced faculty members of the department act as guides and assist the students in process of project design. The students are guided to produce a schematic drawing, a functional product and document a project. Computers are used wherever they can support the project design and documentation, or as part of the working project. Word processors, network simulators, drawing programs, schematic capture, and printed circuit layout, cross assemblers and emulators, and direct digital I/O are all used. Software like MATLAB, PSPICE, MODELSIM, ORCAD, ALTERA VHDL SIMULATOR & SYNTHESIZER, XILINX FOR VHDL are used as per requirement.

The project experience will exercise engineering skills with an emphasis on testing, as well as cost and scheduling for the students. Motivated and Brilliant students are guided to present and publish review/research paper in reputed conference and journals after their projects are documented.