

Papers in Reviewed Journal

1. Rakesh Kumar Saxena, Neelam Sharma, A.K Wadhvani "Performance Analysis of Various Universal Logic Redundant Adders" International journal of Engineering Research & Technology (IJERT), ISSN: 2278-0181, Vol - 4, Issue 8, Aug., 2015.
2. Abhinandan Aggarwall, Gagandeep Singh, Neelam Sharma "Implementation of AES algorithm" International Journal of Engineering Research & Science (IJOER), ISSN, (2395-6992), Vol-2, Issue-4, April – 2016, Page 112.
3. Praween Kumar Sinha, Neelam Sharma, Rohit Mishra "A Configuration for Realizing Voltage Controlled Floating Inductance and Its Application" Circuit & Systems Scientific Research Publications (USA), Vol 6, PP 189-199, 2015.
4. Praween Kumar Sinha, Neelam Sharma, Simran Agarwal, Sudipto Saha "CFOA based Bandpass and Bandstop Ladder Filter – A New Configuration" Circuit & Systems, Scientific Research Publishing, Dec.2015.
5. "Analytical Drain Current Formulation for Gate Dielectric Engineered Dual Material Gate -Gate All Around-Tunneling Field Effect Transistor," Jaya Madan, R.S Gupta, Rishu Chaujar, Japanese Journal of Applied Physics (JJAP), Volume 54, no. 9 August 2015. (In press)
6. Localised Charge Dependent Threshold Voltage Analysis of Gate Material Engineered Junctionless Nanowire Transistor," Yogesh Pratap, S. Haldar, R. S. Gupta and Mridula Gupta, IEEE Transactions on Electron Device, Vol. 62, no. 8, pp. 2598-2605, (August) 2015.
7. Capacitance Modeling of Gate Material Engineered Cylindrical/Surrounded Gate MOSFETs for Sensor Applications," Jay Hind Kumar Verma, Yogesh Pratap, Subhasis Haldar, R. S. Gupta and Mridula Gupta, Superlattices and Microstructures Journal, Volume 88, pp. 271–280, (December) 2015.
8. Modeling and Simulation of Cylindrical Surrounding Double Gate MOSFET for Enhanced Electrostatic Integrity," Jay Hind Kumar Verma, Subhasis Haldar, R. S. Gupta and Mridula Gupta Superlattices and Microstructures Journal vol. 88, pp 354-364, Dec 2015.
9. DS-Schottky Barrier Cylindrical GAA MOSFET: Nan sensor for Biochips," Manoj Kumar, Subhasis Haldar, Mridula Gupta and R.S. Gupta, Nan materials and Energy Journal, vol. 5, issue 1, Jan. 2016.
10. Analytical Modeling of Junctionless Accumulation Mode Cylindrical Surrounding Gate MOSFET (JAM-CSG)", International Journal of Numerical Modelling, Nitin Trivedi, Manoj Kumar, Subhasis Haldar, S. S. Deswal, and R.S. Gupta, DOI: 10.1002/jnm.2162, 2016.

11. Numerical Modeling of Sub threshold Region of Junctionless Double Surrounding Gate MOSFET (JDSG), " Sonam Rewari, Subhasis Haldar, Vandana Nath, S.S. Deswal, and R.S. Gupta. Superlattices and Microstructures Journal, vol. 90, pp 8-19, Dec 2015.
12. Physics based analytical model for surface potential and sub threshold current of cylindrical schottky barrier Gate All Around MOSFET with High-k Gate stack", Manoj Kumar, Subhasis Haldar, Mridula Gupta, and R.S. Gupta. Superlattices and Microstructures Journal, vol. 90, pp 215-226, Jan. 2016.
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15. Praween K. Sinha, Neelam Sharma, Simran Agarwal, Sudipto Saha "CFOA Based Band Stop Ladder Filter – A New Configuration" published in <http://www.scirp.org/journal/cs>, ISSN(Print): 2153-1285, ISSN(Online): 2153-1293, Vol - 7 Page 29-42.
16. Praween Kumar Sinha, Neelam Sharma, Rohit Mishra "A configuration for Realizing Voltage Controlled Floating Inductance and Its Application" Circuit and Systems, Scientific Research Publishing, ISSN (Online): 2153-1293, ISSN (Print): 2153-1285 Vol - 6, Page 189-199, Sep. 2015.
17. Praween Kumar Sinha, Neelam Sharma, Simran Agarwal, Sudipto Saha "CFOA based band pass and band stop ladder filter-a new configuration" Circuit & Systems Scientific Research Publications (USA), Vol 7, PP 29-42, 2016
18. Praween Kumar Sinha, Neelam Sharma, Rohit Mishra "A Configuration for Realizing Voltage Controlled Floating Inductance and Its Application" Circuit & Systems Scientific Research Publications (USA), Vol 6, PP 189-199, 2015.
19. Paramvir Rathee, Neeraj, Shalu garg, "Comparative analysis of wavelet domain techniques for image stenography." (IJAIR2016)VOLUME 5 ISSUE 4.pg-622-627,2016.
20. Paramvir Rathee, Neeraj, " A Survey on different wavelet transform in stenography".(IJAIR 2016),VOL 4 ISSUE 5 PG 268-271,2016.
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22. Numerical Modeling of Sub threshold Region of Junctionless Double Surrounding Gate MOSFET (JDSG), " Sonam Rewari, Subhasis Haldar, Vandana Nath, S.S.

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1. Nikita Sharma, Neelam Sharma, "Low Power CMOS VLSI Design: Implementation of Comparator Circuits using the Novel Technique" Gyanodyan-2015" Changing Technological and Managerial Scenario: Aspects and Prospects. ISBN:978-93-84869-94-6, November 2015.
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5. AC analysis of Junctionless Double Surrounding Gate MOSFT for tetra hertz applications", Sonam Rewari, Vandana Nath, S.S Deswal and R.S. Gupta, ICCTICT, GGSIPU, 11-13 March 2016 , Delhi, India.
6. DMG Insulated Shallow Extension Cylindrical GAA Schottky Barrier MOSFET for Removal of Ambipolarity: A Novel approach" Manoj Kumar, Yogesh Pratap Subhasis Halder, Mridula Gupta and R.S. Gupta, IEEE INEC 2016, Chengdu, China, May 9 –11, 2016.
7. Sensitivity Investigation of Gate All Around Junctionless Transistor for Hydrogen Gas Detection", Yogesh Pratap, Manoj Kumar, S. S. Deswal, Subhasis Halder, R.S. Gupta and Mridula Gupta, IEEE INEC 2016, Chengdu, China, May 9 –11, 2016.
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Vandana Nath, Subhasis Haldar, S.S.Deswal and R.S.Gupta , 3rd International Conference on Microelectronics, Circuits and Systems, Micro2016, 9-10 July, Kolkata, India.

10. Hafnium Oxide Based Cylindrical Junctionless Double Surrounding Gate (CJLDSG) MOSFET for High Speed, High Frequency Digital and Analog Applications," Sonam Rewari, Vandana Nath, Subhasis Haldar, S.S.Deswal and R.S.Gupta, 3rd International Conference on Microelectronics, Circuits and Systems, Micro2016, 9-10 July, Kolkata, India.
11. Hemant Tulsani and Rashmi Gupta. "Wavelet based optimized polynomial threshold function for ECG signal demising." Computing for Sustainable Global Development (Indicator), 2015 2nd International Conference on. IEEE, 2015.
12. TCAD Assessment of Oxide Impact on Linearity and Harmonic Distortions in Gate All Around (GAA) MOSFET," Akansha Goyal, Sonam Rewari, S.S.Deswal and R.S.Gupta, 3rd International Conference on Microelectronics, Circuits and Systems, Micro2016, 9-10 July, Kolkata, India.
13. An Novel Design to Improve Band to Band Tunneling and to reduce Gate Induced Drain Leakages (GIDL) in Cylindrical Gate All Around (GAA) MOSFET," Sonam Rewari, Vandana Nath, Subhasis Haldar, S.S.Deswal and R.S.Gupta, 3rd International Conference on Microelectronics, Circuits and Systems, Micro2016, 9-10 July, Kolkata, India.
14. AC analysis of Junctionless Double Surrounding Gate MOSFT for tetra hertz applications", Sonam Rewari, Vandana Nath, S.S Deswal and R.S. Gupta, ICCTICT, GGSIPU, 11-13 March 2016 , Delhi, India.