

Device Research Laboratory (DRL) Room No.: 402(B)



The Device Research Laboratory (DRL) in the department of Electronics and Communication (ECE) is well equipped. It has two advanced servers and 8 computer system with latest configuration. In addition to all this there is one colour and two normal printers. There are two advanced simulators, one is Silvaco ATLAS 3D device simulator and the other one is Visual TCAD device simulator. The students are working on various devices such as:- Junctionless Double Surrounding Gate (JLDSG) MOSFET, Gate Engineered Gate Stack Schottky Barrier Gate All Around MOSFET and Junctionless Accumulation Mode Cylindrical Surrounding Gate MOSFET. Also at present there is one DRDO project entitled "Analysis and Characterization of Silicon Gate All Around (GAA) nanowire MOSFET for ULSI circuit applications". In 2015 about 12 papers were published in International Journal and Conferences.