

Digital System Design Lab

Block No.4, Room No. 422, ECE Dept., MAIT



The DSD Laboratory is well equipped with industry standard EDA front end and back end tools along with the hardware resources by Xilinx and Mentor Graphics. The lab provides the licensed version of Vivado software a front end tool which provides a new virtual platform to the students, thereby enabling them to explore and practice digital design exercises by simulating and verifying the results. Students can use VHDL or Verilog Hardware Description Language for functional simulation and synthesis of their digital circuits. This lab also provides hardware PYNQ-Z2 FPGA board which can be programmed by the Vivado software to implement various digital designs.

Courses to cater:

- ETEC-351 Digital System Design lab

Facilities:

- Vivado Software by Xilinx
- PYNQ-Z2 FPGA board
- Mentor Graphics HEP1 tools