MAHARAJA AGRASEN INSTITUTE OF TECHNOLOGY

(Department of Electronics and Communication Engineering)

NOTICE

Date: 18 November 2026

Subject: Constitution of Organizing Committee for the Two-Day Seminar on "Innovations in Semiconductor Technology, VLSI Design and Intelligent Computing" scheduled for 18–19 February 2027.

This is to inform all concerned that a two-day seminar titled **"Innovations in Semiconductor Technology, VLSI Design and Intelligent Computing"**, is scheduled to be organized on **18 and 19 February 2027** at Maharaja Agrasen Institute of Technology, Delhi. To ensure the smooth planning, coordination, and successful conduct of the seminar, the following faculty members and staff are hereby nominated as members of the Organizing Committee, with responsibilities assigned as detailed below.

Organizing Committee

1	Dr. Amit Saxena	(Coordinator)
2	Dr. Anamika Jain	(Co-coordinator)
3	Dr. Preeti Goyal	
4	Dr. Sumedha Gupta	
5	Dr. Neeraj	
6	Ms. Shallu	

Supporting Staff

1. Mr. Manoj Bhardwaj
2. Mr. Parveen Saini
3. Mr. Hirdesh

All committee members are requested to coordinate with one another and ensure that the necessary arrangements are completed well in advance. Each committee shall be responsible for the timely execution of its assigned duties to ensure the smooth and successful conduct of the seminar.

Prof. Sunil Kumar
HOD, Dept. of ECE

Cop to:

1. Director MAIT
2. Additional Director MAIT
3. All faculty ECE, MAIT.