

Ms. Swati Sharma



Designation: Assistant Professor

Qualification: Ph.D (Purs.), M.E, B.Tech

Specialization: VLSI Design and Embedded System.

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Education

Ph.D. (Pursuing) “**Analytical Modelling and Simulation Cylindrical Gate All Around MOSFET**” from GGSIPU, Delhi.

M.E in EPDT (Electronics and Communication Engineering Department) from PEC University of technology, 2008-2010.

B.Tech in Electronics and Communication Engineering from Kurukshetra University, 2004-2008.

Research Interest

VLSI Design, Embedded System Design.

Publications

International Journals: 01

International Conferences: 02

List of Publications

- [1]. Swati Sharma, Anubha Goel, Sonam Rewari, Vandana Nath, and R. S. Gupta. "Enhanced Analog Performance and High-Frequency Applications of Dielectric Engineered High-K Schottky Nanowire FET." *Silicon* (2022): 1-17.
- [2]. Sharma, Swati, Sonam Rewari, Vandana Nath, S. S. Deswal, and R. S. Gupta. "Schottky barrier double surrounding gate MOSFET for high-frequency implementation." In *2020 5th IEEE International Conference on Recent Advances and Innovations in Engineering (ICRAIE)*, pp. 1-4. IEEE, 2020.

- [3]. Swati Sharma, Anubha Goel, Sonam Rewari, Vandana Nath and R. S. Gupta” Gallium Nitride Cylindrical Schottky Barrier MOSFET(GaN-CSB-MOSFET) For High-Frequency Implementation." In *2021 International Conference on Industrial Electronics Research and Applications (ICIERA)*. IEEE, 2021.