Dr. K. L. Pushkar

Designation: Assistant Professor

Qualification: Ph.D., M.E., M.E., B.Tech.

Specialization: Analog Signal Processing, Embedded System Design

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Education

Ph. D. in "Some Investigations into the Design of Analog Circuits" from Jamia Millia Islamia Delhi, in 2014.

M.E. in Electronics and Communication Engineering from **IIT Roorkee** (formerly University of Roorkee, Roorkee), in 1999.

M.E. in Electronics and Communication Engineering from **DCE Delhi** (University of Delhi, Delhi), in 2008.

B. Tech. in Electronics Engineering from **IET Lucknow** (University of Lucknow), in 1995.

Research Interest

Analog Signal Processing, Communication Systems

Research Publications

International Journals: 08

Selected Papers

[1] **K. L. Pushkar**, D. R. Bhaskar and D. Prasad, "Voltage-mode new universal biquad filter configuration using a single VDIBA," *Circuits, Systems, Signal, and Processing*, DOI 10.1007/s00034-9625-0., July 2013.

[2] **K. L. Pushkar**, D. R. Bhaskar and D. Prasad, "A new MISO-type voltage-mode universal biquad using single VD-DIBA," *ISRN Electronics*, vol. 2013, Article ID 478213, 5 pages, 2013.

[3] **K. L. Pushkar**, D. R. Bhaskar and D. Prasad, "Voltage-mode universal biquad filter employing single VD-DIBA," *Circuits and Systems*, vol. 4, no. 1, pp. 44-48, Jan. 2013.

[4] D. Prasad, D. R. Bhaskar, and **K. L. Pushkar**, "Electronically controllable sinusoidal oscillator employing CMOS VD-DIBAs," *ISRN Electronics*, vol. 2013, Article ID 823630, 6 pages, 2013.

[5] **K. L. Pushkar**, D. R. Bhaskar and D. Prasad, "Single-resistance controlled sinusoidal oscillator using single VD-DIBA," *Active and Passive Electronic Components*, vol. 2013, Article ID 971936, 5 pages, 2013.

[6] D. R. Bhaskar, D. Prasad, and **K. L. Pushkar**, "Fully uncoupled electronically controllable sinusoidal oscillator employing VD-DIBAs," *Circuits and Systems*, vol. 4, pp. 264-268, 2013.

[7] D. Prasad, D. R. Bhaskar, and **K. L. Pushkar**, "Realization of new electronically controllable grounded and floating simulated inductance circuits using voltage differencing differential input buffered amplifiers," *Active and Passive Electronic Components*, vol. 2011, Article ID 101432, 8 pages, 2011.

[8] D. R. Bhaskar, D. Prasad, and **K. L. Pushkar**, "Electronically-controllable grounded-capacitor-based grounded and floating inductance simulated circuits using VD-DIBAs," *Circuits and Systems*, vol. 4, no. 5, pp. 422-430, Sept. 2013.

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