

Mr. VAIBHAV NIJHAWAN

Designation: Assistant Professor

Qualification: M.Tech., B.Tech.

Specialization: VLSI Design And Embedded Systems

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Education

Masters in VLSI Design and Embedded System from Delhi Technological University, 2013

Bachelors in ECE from IP University, 2010

Research Interest

Logic CMOS Device Design and Characterization, Compact device modeling, Optical burst switching and sonar based applications, VLSI Digital circuit designing viz. ADC, Verilog/VHDL modeling of microprocessors or digital circuits.

Professional Membership: IEEE member