

Mrs. Sonam Rewari

Designation: Assistant Professor

Qualification: Ph.D. (Purs.), M.Tech., B.Tech.

Specialization: VLSI

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Education

Ph. D. Pursuing in Modeling and Simulation Of Gate All Around (GAA) MOSFET from GGSIPU.

M. Tech. in VLSI Design from CDAC-NOIDA, GGSIPU, 2010-2012

B. Tech. in ECE from Kurukshetra University, 2009.

Research Interest

Modeling and Simulation of Gate All Around (GAA) MOSFET

Publications

International Journals: 01

National Journals: 0

International Conferences: 02

National Conferences: 0

Selected Papers

[1]. Sonam Rewari, Subhasis Halder, Vandana Nath, S.S. Deswal, and R.S. Gupta, "Numerical Modeling of Subthreshold Region of Junctionless Double Surrounding Gate MOSFET (JDSG)," Superlattices and Microstructures Journal, vol. 90, pp 8-19, Feb 2016.

[2]. Sonam Rewari, Vandana Nath, S.S. Deswal and R.S. Gupta, "Silicon Carbide based DSG MOSFET for high power high speed and high frequency applications," India International Conference on Power Electronics (IICPE, Kurukshetra, India), December 8-10, 2014.

[3] Sonam Rewari, Vandana Nath, Subhasis Halder S.S Deswal and R.S. Gupta, "AC Analysis of Junctionless Double Surrounding Gate (JLDSG) MOSFET for Tera Hertz Applications", India

International Conference on Power Electronics (ICCTICT, GGSIPU, DELHI, India), March 11-13, 2016.

Books Published

None

Sponsored Projects at hand

None

Thesis Guided:0

Professional Membership: None