

Mr. Priyank Kr. Saxena

Designation: Assistant Professor

Qualification: M.Tech., B.Tech.

Specialization: VLSI Design

Email Id: Priyank.saxena08@gmail.com

Office Address: Room No. 402(B), Dronacharya Block, MAIT, Sec – 22, Rohini,
Delhi – 86



Education

M. Tech. in VLSI Design from DTU, Delhi, 2012

B. Tech. in ECE from UPTU, 2009

Research Interest

VLSI, Analog circuits

Thesis Guided: 11