

Ms. Preeti Goyal

Designation: Assistant Professor

Qualification: M.Tech., B.Tech.

Specialization: VLSI

Email Id: preetigoyal.s@gmail.com

Office Address: Room No. 445, 4th Block, MAIT, Sec – 22, Rohini, Delhi – 86



Education

Masters in VLSI Design from CDAC Noida in 2007

Bachelors in ECE from MDU, 2005

Research Interest

VLSI, Digital

Publications

National Journals: 1

Selected Papers

[1]. Area Efficient FPGA Implementation of AES-128, //ICFAI Journal of Science & Technology, Sep 2008, Vol 4 Issue 3, P17