

# **Mr. Praveen Kumar**

**Designation:** Assistant Professor

**Qualification:** M.Tech., B.Sc(Engg.)

**Specialization:** VLSI

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## ***Education***

Masters in VLSI Design from Shobhit university , 2010

Bachelors in Electronics Engineering from Ranchi University,2003

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## ***Research Interest***

Analog Signal Processing, Digital System Design and Microelectronics.

## ***Publications***

**International Conferences:** 04

## ***Selected Papers***

1. “Algorithm Design for Generation of Fault Dictionary in Analog VLSI Circuits.” Advances in Recent Technologies in Communication and Computing (ARTCom), 2010 International Conference. Pp. 374-376. Oct 2010.
2. “Alignment of DNA Sequence Using the Features of Global and Local Algorithms along with Matrices”. Advanced Materials Research, Vols. 403-408, pp. 2012-2015, Nov. 2011.
3. “A new approach for testing CMOS circuits for glitches” Computer Design and Applications (ICCD), 2010 International Conference. Pp. V3-595 - V3-598, June 2010.
4. “A new approach to find the alignment of DNA sequence using matrices”. Computer Science and Information Technology (ICCSIT), 2010 3rd IEEE International Conference on (Volume:7 ). Pp. 451 – 454, July 2010.

**Professional Membership:** IEEE