

Brief Profile

Vaibhav Nijhawan

Designation: Assistant Professor

Qualification: Pursuing PhD, M.Tech., B.Tech.

Email Id: vaibh99@gmail.com, vaibhavnijhawan@mait.ac.in

Contact Number: 09811556771(M)

Specialization: VLSI Design and Embedded Systems, Microprocessors, Measurement.

Office Address: Room No. 441, 4 th Block, MAIT, Sec – 22, Rohini, Delhi – 86

Education: Masters in VLSI Design and Embedded System from Delhi Technological University, 2013 Bachelors in ECE from IP University, 2010

Research Interest: Logic CMOS Device Design and Characterization, Compact device modeling, VLSI Digital circuit designing viz. ADC, Embedded System Design, IoT applications, Computer Vision, ML.

Number of Faculty Development Programme/ Training courses attended: 17

Number of Seminars/ Workshop/ Courses attended: 18

Number of conferences attended: 07

Number of Webinars attended: 54

COMPUTER SKILLS

Languages :

C, C++, VHDL, Verilog(Basics), Pspice, Cadence Virtuoso, MATLAB, SCILAB, Xilinx ISE, Multisim, MS Office, Python.

- Embedded : 8051, 8086, 8085, MSP 430, Embedded C, Assembly.

PUBLICATIONS

Papers in International Conferences/ Journal

1. Vaibhav, Hemant Tulsani, "Low Power High Speed Current Conveyor based Current Comparator, Mirror Circuit and Encoder used in ADCs", IEEE International Conference on Computing for Sustainable Global Development, Mar. 2017
2. Vaibhav, Davinder Miglani, Karan, Shivam Gupta " Design of Low Power High Speed Current Comparator Based Flash ADC ", IEEE International Conference on Computing for Sustainable Global Development, Mar. 2018.
3. Parv Rustogi, Dhruv Agarwal, Aditya Rastogi, Vaibhav Nijhawan "Real Time RF Based Gesture Controlled Robotic Vehicle", INDIAComm, 2021.(IEEE Xplore)
4. Deepanshu Tanwar, Vaibhav Nijhawan, Pragya Singh, Rashi Gupta "Design of Low cost Woman Safety System with GPS and GSM", INDIAComm, 2021. (IEEE Xplore)
5. Anshul Vashist, Romanch Kansal, Vaibhav Nijhawan, Zaid Zafar, "A Fingerprint Based Ignition System in Vehicles", International Journal of Scientific & Engineering Research Volume 12, Issue 6, June-2021 440 ISSN 2229-5518.
6. Vignesh Singh, Rishika Anand, Dhruv Anand, Vaibhav Nijhawan, "Home Environment Monitoring System with an alert", ICIERA, 2021.(IEEE Xplore)
7. Vaibhav, Davinder Miglani, Karan, Shivam Gupta " Design of Low Power High Speed Current Comparator Based Flash ADC ", IEEE International Conference on Computing for Sustainable Global Development, Mar. 2018. (IEEE Xplore)

EXPERIENCE

Total Experience:- 8 year 11 months

PROFESSIONAL PARTICIPATION

1. IEEE Member
2. Member, Vijnanabharati (Vibha)